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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Norman et al.
Title: Device and Method for Controlling Solid-State Memory System
Application No.: 10/785,373 Filing Date: February 23, 2004
Examiner: Unknown Group Art Unit: 2824
Docket No.: SNDK.015US6 Conf. No.: 8958

Certificate of Mailing Under 37 CFR 1.8

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Signature Gileen Rowe

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicants call the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

This application has a filing date after June 30, 2003. Copies of the U.S. Patents listed on the accompanying Form PTO-1449 are not enclosed. Copies of the Other Art listed on the accompanying Form PTO-1440 are enclosed.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or

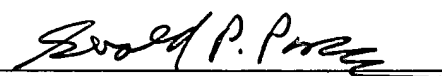
Attorney Docket No.: SNDK.015US6

Application No.: 10/785,373

(3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664. This form is being submitted in duplicate.

Respectfully submitted,



Gerald P. Parsons
Reg. No. 24,486

Nov. 19, 2004
Date

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Sheet 1 of 1

U.S. Department of Commerce, Patent and Trademark	Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	SN DK.015US6	10/785,373
	Applicant(s)	Conf. No.
(Use several sheets if necessary)	Lofgren et al.	8958
(Form PTO-1449)	Filing Date	Group
	February 23, 2004	2824

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	4,614,194	9/86	Jones et al.			
	2	4,616,655	10/86	Weinberg et al.			
	3	5,028,986	7/91	Sugano et al.			
	4	5,128,831	7/92	Fox, III et al.			
	5	5,347,428	9/94	Carson et al.			
	6	5,373,189	12/94	Massit et al.			
	7	5,563,828	10/96	Hasbun et al.			
	8	5,581,498	12/96	Ludwig et al.			
	9	5,674,260	10/97	Weinberg			
	10	5,734,559	3/98	Banerjee et al.			
	11	5,818,107	10/98	Pierson et al.			
	12	5,987,357	11/16/99	Prutchi et al.			
	13	6,208,579	3/27/01	Prutchi et al.			

U.S. Published Patent Application Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	14	"8 Megabit High Speed CMOS SRAM", <i>Dense-Pac Micro-system</i> , DPS512x16MKn3, pp. 1-8, (Feb. 1996).
	15	Tuckerman et al., "Laminated Memory: A New-3-Dimensional Packaging Technology for MCMs," <i>IEEE</i> , 1994, pp. 58-63 (1994).

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.